Data Security using Patch based Approach for Texture Synthesis

Mr. Dattu. B. Zambare
S.N.D.COE&RC, Yeola.

Prof. V. N. Dhakane
S.N.D.COE&RC, Yeola.

ABSTRACT
This dissertation work proposes scheme Data security using patch based approach for Texture Synthesis. System uses the concept of patch which represents an image block of source texture and its size is user defined. A texture synthesis process resamples a smaller texture image, which synthesizes a new texture image with a similar appearance and arbitrary size. The texture synthesis process is weaved into steganography to hide secret messages. In contrast to using an existing cover image to hide messages, the algorithm hides the source texture image and embeds secret messages using the process of texture synthesis. This allows extracting the original secret messages and source texture from a stego synthetic texture. The approach offers some advantages. First, the scheme offers the embedding capacity that is proportional to the size of the stego texture image. Second, the reversible capability inherited from this scheme provides functionality, which allows recovery of the source texture.

Keywords
Data embedding, patch, reversible, steganography, texture synthesis, stego synthetic texture.

INTRODUCTION
Steganography [2] is process of concealing or hiding a file, message, image or video within another file, message, image, or video. The steganographic application includes not openly acknowledged or displayed communication between two parties whose existence is unknown to possible attacker and whose success depends on detecting the existence of this communication [3]. Many of the image steganographic algorithms adopt an existing image as cover medium. The embedding of secret messages into the cover image can lead to image distortion in the stego image. This paper proposes Data security using patch based approach for Texture Synthesis. It uses the concept of patch which represents an image block of source texture where its size is user specified or defined. A texture synthesis process resamples a smaller texture image, which synthesizes a new texture image with a similar appearance and arbitrary size. The texture synthesis process is weaved into steganography to hide secret messages. In contrast to using an existing cover image to hide messages, the algorithm conceals the source texture image and embeds secret messages using the process of texture synthesis. This allows extracting the secret messages and source texture from a stego synthetic texture. The approach offers some advantages. First, the scheme offers the embedding capacity that is proportional to the size of the stego texture image. Second, the reversible capability inherited from this scheme provides functionality, which allows recovery of the source texture.

LITERATURE SURVEY
Cohen et al. and Xuet al. [4], [5] uses the patch-based approach. Patch-based algorithms paste patches from a source texture instead of pixel to form the synthesized texture.
Advantage:
- This approach improves the image quality of pixel-based synthetic textures as texture structures inside the patches are maintained.

Disadvantage:
- Since patches are pasted with a small overlapped region during the synthetic process, one needs to make an effort to ensure that the patches agree with their neighbors.

Liang et al. [6] proposed the patch-based sampling and used the feathering approach for the overlapped areas of adjacent patches.

Advantage:
- The patch-based sampling algorithm is fast and it makes high-quality texture synthesis.
- The patch-based sampling algorithm works well for a wide variety of textures ranging from regular to stochastic.

Efros and Freeman [7] proposed a patch stitching approach called “image quilting for texture synthesis.”

Advantage:
- Quilting is new, fast, yet very simple texture synthesis algorithm which produces good results for a wide range of textures.
- A dynamic programming technique is used to disclose the minimum error path through the overlapped region.

Ni et al. [8] introduces an image reversible data hiding algorithm which can recover the cover image without any distortion from the stego image after the hidden data have been extracted.

Advantages:
- Recover source texture without any distortion.

Ni et al. [9] proposed a general framework of current state of the art for reversible image data hiding.

SYSTEM OVERVIEW

Fig 1: System Overview of Data security using patch based approach for Texture Synthesis.
Fig.1 Shows System Overview of Data security using patch based approach for texture synthesis. It consists of selecting texture and generating patches, message embedding, capacity determination, and source texture recovery and message extraction.

A. **Scope:**
- Major parts of system will include Texture synthesis, message embedding and Source texture recovery, Message extraction, Message authentication.
- To develop a system which can be easily embed in different applications where security is the main concern.
- To implement a system this will reduce overheads of text or image encryption algorithms.
- To develop a system this will retain the quality of service and the system performance.

B. **Objective:**
- To generate texture image patches.
- To generate Index table and Composite image.
- To embed message in the source texture without disturbing quality of the texture.
- To extract the data with no change.

**BREAKDOWN STRUCTURE**

![System Breakdown structure](image)

**Fig 2: System Breakdown structure.**

A. **Select Texture and Generate Patch Module:**
- Select the texture which is to be used as cover medium.
- Here the concept of patch is used. Patch is image block of source texture where its size is user specified. Patch size is denoted by its width and height.
- Then the concept of kernel block is used, which formed by subdividing the source texture into a number of non-overlapping kernel block each of which has size of kernel width and kernel height.
Divide all the prediction errors into $L$ clusters. The source patch is formed by expanding the kernel block with the depth at each side to produce a source patch.

$$SP_n = \frac{S_w}{K_w} \times \frac{S_h}{K_h}$$

Here, $SP_n$ is no of source patches, $S_w$ and $S_h$ is source texture width and height, $K_w$ and $K_h$ is kernel width and height.

**B. Message Embedding Module:**

- The index table generation is carried out first where we produce an index table to record the location of source patch. The index table allows us to access the synthetic texture and retrieve the source texture completely. This reversible embedding style reveals one of the major benefits. The index table has the initial values of -1 for each entry, that shows the table is blank. We then assign values after distributing the source patch ID in the synthetic texture.
- In patch composition process we paste the source patches into the workbench to produce composite image.

After generation of index table and composition image and after pasting source patches into workbench, we will embed the secret message via message oriented texture synthesis to produce stego synthetic texture.

$$TP_n = T_{pw} \times T_{ph} = \left( \frac{(W - P_w)}{P_w - P_p} + 1 \right) \times \left( \frac{(H - P_h)}{P_h - P_p} + 1 \right)$$

$TP_n$ is number of patches in synthetic texture, $Tw$ and $Th$ is synthetic texture width and height, $Pd$ is patch depth.

**C. Capacity Determination Module:**

- Maximum capacity in bits/patch is calculated.
- Calculate number of embeddable patches which is difference between the number of patches in the synthetic texture and number of source patches subdivided from source texture.
- Calculate total embedding capacity which is product of maximum capacity in bits/patch and number of embeddable patches.
Total Capacity= Bits per pixel that can be embedded in each patch X number of embeddable patches.

D. Source Texture Recovery, Message extraction, and Message Authentication:
- Generate the index table, given the secret key held in receiver side. The same index table as the embedding procedure can be generated.
- Recover the source texture. Source texture can be recovered or retrieved by referring the index table, then we arrange the blocks based on the order. Hence, the recovered texture will be same as the source texture.
- Generate the composite image, by pasting source patches into the workbench by referring the index table.
- Extract message by constructing candidate list and then perform match authentication step.

RESULT ANALYSIS
A. Result of Embedding Capacity:

Table 1 shows total embedding capacity in bits that can be provided when different resolutions of the synthetic texture are produced by concealing various BPPs. It is important to point out that given a fixed number of BPP, the larger the resolutions of the source texture Sw x Sh (128 x 128 vs. 300 x300), the smaller the total embedding capacity (TC). This is because the larger source texture will contain more source patches SPn (9 vs. 51) that we need to paste which cannot conceal any secret bits. This will reduce the number of embeddable patches (EPn).

<table>
<thead>
<tr>
<th>Pw * Ph = 58 * 58</th>
<th>Tw * Th = 500 * 500</th>
<th>Pd = 8</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sh*Sw</td>
<td>SPn</td>
<td>Epn</td>
</tr>
<tr>
<td>128*128</td>
<td>9</td>
<td>87</td>
</tr>
<tr>
<td>250*250</td>
<td>35</td>
<td>61</td>
</tr>
<tr>
<td>300*300</td>
<td>51</td>
<td>45</td>
</tr>
</tbody>
</table>

Table 2. Computing Time (Second)

<table>
<thead>
<tr>
<th>Capacity</th>
<th>Pure</th>
<th>5BPP</th>
<th>10BPP</th>
<th>BPPmax</th>
</tr>
</thead>
<tbody>
<tr>
<td>128*128</td>
<td>780</td>
<td>1200</td>
<td>1325</td>
<td>1262</td>
</tr>
<tr>
<td>250*250</td>
<td>931</td>
<td>1580</td>
<td>1622</td>
<td>1600</td>
</tr>
<tr>
<td>300*300</td>
<td>1100</td>
<td>1820</td>
<td>2050</td>
<td>2112</td>
</tr>
</tbody>
</table>

Fig 5: Graph for Total embedding capacity in bits that algorithm can provide.

Maximum embedding capacities with varying source texture and stego synthetic texture size.
B. **Image Quality Comparison:**

1) **Mean Squared Error for Overlapped area (MSEO):**

<table>
<thead>
<tr>
<th></th>
<th>Pure</th>
<th>5BPP</th>
<th>10BPP</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rope net</td>
<td>6842</td>
<td>6846</td>
<td>6790</td>
</tr>
<tr>
<td>Metal</td>
<td>8763</td>
<td>8768</td>
<td>8919</td>
</tr>
<tr>
<td>Peanuts</td>
<td>2832</td>
<td>2837</td>
<td>2913</td>
</tr>
<tr>
<td>Ganache</td>
<td>1116</td>
<td>1125</td>
<td>1204</td>
</tr>
</tbody>
</table>

Mechanism to determine image quality is MSEO. The MSEO has a non-zero value even in the case of the pure patch based texture synthesis. If the MSEO produces a small value, it implies that the synthetic texture shows a high image quality of the overlapped areas. Obviously, the lower the MSEO value, the higher quality of the synthetic texture image.

2) **Structural SIMilarity index (SSIM):**

SSIM (Structural SIMilarity) index used to quantify the similarity between the pure and stego synthetic textures. The SSIM is an image quality assessment method for measuring the change in luminance, contrast, and structure in an image. The SSIM index is in the range of \([-1, 1]\) and when it equals to 1, the two images are identical.

<table>
<thead>
<tr>
<th></th>
<th>Pure vs.</th>
<th>5BPP</th>
<th>10BPP</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rope net</td>
<td>0.0191</td>
<td>0.0305</td>
<td></td>
</tr>
<tr>
<td>Metal</td>
<td>0.0161</td>
<td>0.0012</td>
<td></td>
</tr>
<tr>
<td>Peanuts</td>
<td>0.032</td>
<td>0.052</td>
<td></td>
</tr>
<tr>
<td>Ganache</td>
<td>0.0201</td>
<td>0.0673</td>
<td></td>
</tr>
</tbody>
</table>
CONCLUSION

This paper proposes the Data security using patch based approach for texture synthesis. The source texture is given and the system produces the stego synthetic texture can hiding the secret message. The patch based concept is used instead of pixel based scheme. It provides the reversible approach to recover original source texture and secret message from stego synthetic texture.

ACKNOWLEDGMENT

“Data Security using patch based approach for texture synthesis” has been a wonderful subject to research. I dedicate all my dissertation works to my esteemed guide, Prof. V. N. Dhakane, whose interest and guidance helped me to complete the work successfully. I also extend my gratitude to Prof. I. R. Shaikh (H.O.D. Computer Engineering Department) and Prof. V. N. Dhakane (P. G. Coordinator) who has provided facilities to explore the subject with more enthusiasm.

REFERENCES


